

All signals are as seen by IHU (RX, TX, etc)

H1			
Pin #	Pin #		
2	1		
4	3		
6	5		
8	7		
10	9		
12	11		
14	13		
16	15		
18	17		
tlm4	20	19	
tlm2	22	21	tlm3
	24	23	tlm1
	26	25	
GND	28	27	--[R0]-- I1
	30	29	i2c1_reset
GND	32	31	GND
	34	33	
	36	35	
	38	37	
rx3 --[R2]--	40	39	--[R1]-- tx3
GND	42	41	i2c1_sda
cmd_h/w_bit_3	44	43	i2c1_sck
	46	45	abf_(alt)
3v3_sw_1 (NC)	48	47	5v_sw_1 (NC)
	50	49	
dplus --[R4]--	52	51	--[R3]-- dminus

H2			
Pin #	Pin #		
2	1		
4	3		
6	5		
8	7		
10	9		
12	11		
gpio4	14	13	umbilical_attached
gpio2	16	15	gpio3
	18	17	gpio1
	20	19	
rx3	22	21	tx3
	24	23	
v_sys	26	25	v_sys
v_sys --[R6]--	28	27	--[R5]-- v_sys
GND	30	29	GND
GND	32	31	
	34	33	
	36	35	
	38	37	
GND	40	39	GND
GND	42	41	GND
GND	44	43	GND
VBATT(NC)	46	45	VBATT(NC)
	48	47	
abf_placed	50	49	
bus_canl	52	51	--[R7]-- bus_canh

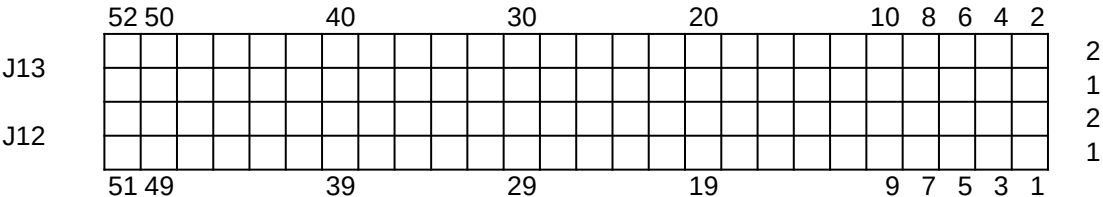
Notes

UART TX, RX from ICD Fig. 4-3. Used by umbilical cord

Vsys = 5v reg enabled at start from ISIS EPS

VBATT for id only – not used

abf_placed = ABF[0], input to MCU



Physical view of IHU PC104 connector pins from **top**
to align with ISISPACE CSKB light sockets